

PATENT  
ONS00149IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re Application of: Phillip C. Celaya et al  
Serial No.: 09/817330  
Filing Date: 3/26/2001  
Group Art Unit: 3729  
Examiner: Thiem D. Phan  
Title: METHOD OF MAKING A LEAD-FREE  
INTEGRATED CIRCUIT PACKAGE

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING FACSIMILE  
TRANSMITTED TO THE PATENT AND TRADEMARK OFFICE,  
ATTN EXAMINER Thiem D. Phan AT THE FAX NO. (703) 872-9306

ON: February 25, 2004  
Date of Facsimile Transmission

LYDIA MCNAMARA  
Name of Person Signing Certificate  
Lydia McNamara 2/25/04  
SIGNATURE DATE

NOTICE OF APPEAL TO THE  
BOARD OF PATENT APPEALS AND INTERFERENCES

HONORABLE ASSISTANT COMMISSIONER FOR PATENTS,  
Alexandria, VA 22313-1450

SIR:

In accordance with 37 CFR 1.191, applicants hereby appeal  
to the Board of Patent Appeals and Interferences from the  
final rejection by the Examiner of claims 19-37.

Please charge the cost of the Appeal (\$330.00) to Deposit  
Account No. 50-1086.

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Respectfully submitted,  
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